

Appl. No. 10/087,632

Amdt. Dated June 22, 2004

Reply to Office Action of April 2, 2004

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the above-identified application:

Claim 1: (original) A circuit for controlling current flow through a solenoid, comprising:

a first controllable switch electrically coupled in parallel with a first resistive element;

a second controllable switch electrically coupled in series with a second resistor;

a diode electrically coupled in series between the first controllable switch and the second controllable switch; and

a controller circuit operable to (i) selectively open and close the first controllable switch and the second controllable switch in response to a command signal and (ii) selectively open and close the second controllable switch based on a magnitude of current flow through the solenoid.

Claim 2: (original) The circuit of Claim 1, wherein the controller circuit comprises:

a comparator circuit having at least a first input terminal coupled to a reference voltage, a second input terminal coupled to receive a voltage signal representative of a current magnitude flowing through the solenoid, and a third input terminal coupled to receive the command signal, the comparator operable to supply a switch control signal based on the signals on the first, second, and third input terminals; and

a driver circuit having at least a first input coupled to receive the switch control signal and operable, in response thereto, to selectively open and close the second controllable switch.

Claim 3: (original) The circuit of Claim 2, wherein the driver circuit further has at least a second input coupled to receive the command signal and operable, in response thereto, to selectively open and close the first controllable switch and the second controllable switch.

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Claim 4: (original) The circuit of Claim 3, wherein the controller circuit further comprises an input circuit operable to condition the command signal and supply the conditioned command signal to the driver circuit second input and the comparator circuit third input.

Claim 5: (original) The circuit of Claim 2, wherein the comparator circuit comprises:
a comparator having a first comparator input terminal electrically coupled to the comparator circuit first input terminal and a second comparator input terminal electrically coupled to the comparator circuit second input terminal, and a comparator output terminal; and

a logical AND circuit having a first AND circuit input coupled to the third comparator circuit input and a second AND circuit input coupled to the comparator output terminal and operable, in response thereto, to supply the switch control signal.

Claim 6: (original) The circuit of Claim 2, wherein the controller circuit further comprises:

a solenoid current monitoring circuit electrically coupled to receive a signal representative of current magnitude flowing through the solenoid and operable, in response thereto, to supply the voltage signal representative of a current magnitude flowing through the solenoid.

Claim 7: (original) The circuit of Claim 6, wherein the solenoid current monitoring circuit comprises:

an amplifier circuit electrically coupled to receive the signal representative of the current magnitude flowing through the solenoid and operable to provide an amplified current magnitude signal; and

a timing circuit coupled to receive the amplified current magnitude signal and operable to supply the a voltage signal representative of a current magnitude flowing through the solenoid.

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Claim 8: (original) The circuit of Claim 1, wherein the controller supplies a first switch control signal to cause the first controllable switch to selectively open and close and a second switch control signal to cause the second controllable switch to open and close.

Claim 9: (original) The circuit of Claim 8, wherein:

the first controllable switch comprises a first MOSFET having at least a gate terminal electrically coupled to receive the first switch control signal, a source terminal electrically coupled to one terminal of the first resistive element, and a drain terminal electrically coupled to another terminal of the first resistive element; and

the second controllable switch comprises a second MOSFET having at least a gate terminal electrically coupled to receive the second switch control signal, a source terminal electrically coupled to one terminal of the second resistor, and a drain terminal electrically coupled to a terminal of the diode.

Claim 10: (original) The circuit of Claim 1, wherein:

current flows through the solenoid, the second controllable switch, and the second resistor when the first controllable switch and the second controllable switch are closed; and

current flows through the solenoid, the diode, and the first controllable switch when the first controllable switch is closed and the second controllable switch is open.

Claim 11: (original) The circuit of Claim 10, wherein current flows through the solenoid, the diode, and the first resistive element when the first and second controllable switches are open after the being closed simultaneously for a predetermined time period.

Claim 12: (currently amended) The circuit of Claim 1, further comprising:

a solenoid ~~coupled~~ electrically coupled in parallel with the first controllable switch and the diode.

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Claim 13: (original) A circuit for controlling current flow through a solenoid, comprising:

a diode having at least a first diode terminal and a second diode terminal;

a first controllable switch electrically coupled in series between the first diode terminal and a first circuit terminal, the first circuit terminal adapted for coupling the circuit to a first predetermined voltage magnitude;

a first resistive element having at least a first resistive element terminal and a second resistor terminal, the second resistor terminal electrically coupled to a second circuit terminal that is adapted for coupling the circuit to a second predetermined voltage magnitude;

a second controllable switch electrically coupled in series between the second diode terminal and the first resistive element terminal; and

a controller circuit having at least a first input coupled to receive a command signal and operable to (i) selectively open and close the first controllable switch and the second controllable switch in response to the command signal and (ii) selectively open and close the second controllable switch based on a magnitude of current flowing through the solenoid.

Claim 14: (original) The circuit of Claim 13, wherein the controller circuit comprises:

a comparator circuit having at least a first input terminal coupled to a reference voltage, a second input terminal coupled to receive a voltage signal representative of a current magnitude flowing through the solenoid, and a third input terminal coupled to receive the command signal, the comparator operable to supply a switch control signal based on the signals on the first, second, and third input terminals; and

a driver circuit having at least a first input coupled to receive the switch control signal and operable, in response thereto, to selectively open and close the second controllable switch.

Claim 15: (original) The circuit of Claim 14, wherein the driver circuit further has at least a second input coupled to receive the command signal and operable, in response

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thereto, to selectively open and close the first controllable switch and the second controllable switch.

Claim 16: (original) The circuit of Claim 15, wherein the controller circuit further comprises an input circuit operable to condition the command signal and supply the conditioned command signal to the driver circuit second input and the comparator circuit third input.

Claim 17: (original) The circuit of Claim 14, wherein the comparator circuit comprises:

- a comparator having a first comparator input terminal electrically coupled to the comparator circuit first input terminal and a second comparator input terminal electrically coupled to the comparator circuit second input terminal, and a comparator output terminal; and

- a logical AND circuit having a first AND circuit input coupled to the third comparator circuit input and a second AND circuit input coupled to the comparator output terminal and operable, in response thereto, to supply the switch control signal.

Claim 18: (original) The circuit of Claim 14, wherein the controller circuit further comprises:

- a solenoid current monitoring circuit electrically coupled to receive a signal representative of current magnitude flowing through the solenoid and operable, in response thereto, to supply the voltage signal representative of the current magnitude flowing through the solenoid.

Claim 19: (original) The circuit of Claim 18, wherein the solenoid current monitoring circuit comprises:

- an amplifier circuit electrically coupled to receive the signal representative of the current magnitude flowing through the solenoid and operable to provide an amplified current magnitude signal; and

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a timing circuit coupled to receive the amplified current magnitude signal and operable to supply the voltage signal representative of the current magnitude flowing through the solenoid.

Claim 20: (original) The circuit of Claim 14, wherein the controller supplies a first switch control signal to cause the first controllable switch to selectively open and close and a second switch control signal to cause the second controllable switch to open and close.

Claim 21: (original) The circuit of Claim 20, wherein:

the first controllable switch comprises a first MOSFET having at least a gate terminal electrically coupled to receive the first switch control signal, a source terminal electrically coupled to one terminal of the first resistive element, and a drain terminal electrically coupled to another terminal of the first resistive element; and

the second controllable switch comprises a second MOSFET having at least a gate terminal electrically coupled to receive the second switch control signal, a source terminal electrically coupled to one terminal of the second resistor, and a drain terminal electrically coupled to a terminal of the diode.

Claim 22: (original) The circuit of Claim 13, wherein:

the current flowing through the solenoid flows through the second controllable switch and the second resistor when the first controllable switch and the second controllable switch are closed; and

the current flowing through the solenoid, flows through the diode and the first controllable switch when the first controllable switch is closed and the second controllable switch is open.

Claim 23: (original) The circuit of Claim 22, wherein the current flowing through the solenoid, flows through the diode and the first resistive element when the first and second

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controllable switches are open after the being closed simultaneously for a predetermined time period.

Claim 24: (original) The circuit of Claim 13, further comprising:

a solenoid coupled electrically coupled in parallel with the first controllable switch and the diode.

Claim 25: (currently amended) A circuit for controlling current flow through a solenoid, comprising:

a first MOSFET having at least a gate terminal, a source terminal, and a drain terminal;

a first resistive element having a first terminal coupled to the first MOSFET source terminal and a second terminal electrically coupled to the first MOSFET drain terminal;

a second MOSFET having at least a gate terminal, a source terminal, and a drain terminal;

a second resistor having a first terminal coupled to the second MOSFET source terminal;

a diode electrically coupled in series between the first MOSFET drain terminal and the second MOSFET source terminal; and

a comparator circuit having at least a first input terminal coupled to a reference voltage, a second input terminal coupled to receive a voltage signal representative of a current flowing through the solenoid, and a third input terminal coupled to receive the command signal, the comparator operable to supply a switch control signal based on the signals on the first, second, and third input terminals;

a solenoid current monitoring circuit electrically coupled to receive a signal representative of a magnitude of current flowing through the solenoid and operable, in response thereto, to supply the voltage signal representative of the current flowing through the solenoid; and

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a driver circuit having at least a first input coupled to receive a command signal and a second input coupled to receive the switch control signal ~~and operable~~, the driver circuit operable to selectively supply a first gate signal to the first MOSFET gate terminal in response to the command signal and to selectively supply a gate signal to the second MOSFET gate terminal in response to the switch control signal.

Claims 26-30: (canceled).